Plot No. 2, Sector - 17A, Yamuna Expressway, Greater Noida, Distt.: Gautam Budha Nagar, Uttar Pradesh (India) 203201

Website: www.galgotiasuniversity.edu.in

## **School Name: School of Engineering**

## Department Name: Department of Electrical, Electronics & Communication EngineeringProgram Name: M.Tech. (VLSI DESIGN)

Scheme: 2018-2020

		Scheme: 2018-2020 SEMESTER I					
Sl. No.	Course Code	Course Title		L	T	P	С
1	MATH5001	Advanced Numerical & Statistical Methods	s (UC)	3	1	0	4
2	MVLS5020	Advanced Digital System Design	(PC)	3	0	0	3
3	MVLS5021	Advanced VLSI Design	(PC)	3	0	0	3
4	MVLS5004	Analog Filter Design	(PC)	3	0	0	3
5	MVLS5005	MOS Device Modelling	(PE)	3	0	0	3
6	MVLS5019	Advanced Digital System Design Lab	(PC)	0	0	2	1
7	MVLS5022	Advancec VLSI Design Lab	(PC)	0	0	2	1
		SEMESTER II					
Sl. No.	Course code	Course Title		L	T	P	С
1	CENG5001	Professional and Communication skills	(UC)	0	0	4	2
2	MVLS5007	DSP for VLSI	(PC)	3	0	0	3
3	MVLS5008	VLSI Testing and fault Tolerance	(PC)	3	0	0	3
4	MVLS5009	ASIC Design and FPGAs	(PC)	3	0	0	3
5	MVLS5010	Advanced Digital VLSI Design	(PE)	3	0	0	3
6	MVLS5011	Low power VLSI Design	(PE)	3	0	0	3
7	MVLS5012	ASIC Design Lab	(PC)	0	0	2	1
8	MVLS5013	DSP for VLSI Lab	(PC)	0	0	2	1
		SEMESTER III					
Sl. No.	Course Code	Course Title		L	Т	P	С
1	MVLS6001	Embedded System Design	(PC)	3	0	0	3
2	MVLS6002	Physical Design Automation	(PE)	3	0	0	3
3	MVLS6003	System on Chip Design	(PE)	3	0	0	3
4	MVLS6004	Embedded System Lab	(PC)	0	0	2	1
5	MVLS9997	Research Seminar	(PC)	0	0	2	1
6	MVLS9998	Capstone Design- 1	(PC)	0	0	10	5
		SEMESTER IV					
Sl. No.	Course Code	Course Title		L	Т	P	C
1	MVLS9999	Capstone Design- 2	(PC)	0	0	30	15