Plot No. 2, Sector - 17A, Yamuna Expressway, Greater Noida, Distt.: Gautam Budha Nagar, Uttar Pradesh (India) 203201 Website: www.galgotiasuniversity.edu.in

School Name: School of Engineering
Department Name: Department of Electrical, Electronics & Communication

EngineeringProgram Name: M.Tech. (VLSI DESIGN) Scheme: 2016-2018

		SEMESTER I				
Sl. No.	Course Code	Course Title	L	Т	P	С
1	MAT502	Advanced Numerical & Statistical Methods	3	1	0	4
2	ECE514	Analog Filter Design	3	0	0	3
3	ECE516	Fundamentals of VLSI Design	3	0	0	3
4	ECE515	Advanced Digital Design Using Verilog	3	0	0	3
5	ECE518	MOS Device Modelling (PE-1)	3	0	0	3
6	ECE557	VLSI Design Lab	0	0	2	1
7	ECE517	VLSI Technology	3	0	0	3
	•	SEMESTER II		1		
Sl. No.	Course code	Course Title	L	Т	P	С
1	ENG584	Professional and Communication skills	0	0	4	2
2	ECE528	DSP for VLSI	3	0	0	3
3	ECE546	DSP for VLSI Lab	0	0	2	1
4	ECE529	ASIC Design and FPGAs	3	0	0	3
5	ECE547	ASIC Design Lab	0	0	2	1
6	ECE525	Low power VLSI Design (PE-2)	3	0	0	3
7	ECE527	Advanced Digital VLSI Design (PE-3)	3	0	0	3
8	ECE526	VLSI Testing and fault Tolerance	3	0	0	3
		SEMESTER III				
Sl. No.	Course Code	Course Title	L	Т	P	С
1	ECE614	Advanced Embedded System Design	3	0	0	3
2	ECE615	Physical Design Automation (PE-4)	3	0	0	3
3	ECE617	System on Chip Design (PE-5)	3	0	0	3
4	ECE654	Advanced Embedded System Design Lab	0	0	2	1
5	ECE619	Project-1	0	0	10	5
	-	SEMESTER IV	-	-	-	-
Sl. No.	Course Code	Course Title	L	Т	P	С
1	ECE629	Project-2	0	0	30	15
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