

**School Name: School of Engineering**  
**Department Name: Department of Electrical, Electronics & Communication**  
**Engineering Program Name: M.Tech. (VLSI DESIGN)**  
**Scheme: 2015-2017**

<b>SEMESTER I</b>						
<b>Sl. No.</b>	<b>Course Code</b>	<b>Course Title</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
1	MAT 502	Advanced Numerical & Statistical Methods	3	1	0	4
2	ECE 525	Modelling and Analysis of Semiconductor Devices	3	0	0	3
3	ECE 526	Fundamentals of VLSI Design	3	0	0	3
4	ECE 527	VLSI Technology	3	0	0	3
5	ECE555	VLSI Design Lab	0	0	2	1
6	ECE 547	MOS Device Modelling (PE-1)	3	0	0	3
<b>SEMESTER II</b>						
<b>Sl. No.</b>	<b>Course code</b>	<b>Course Title</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
1	ENG584	Professional and Communication skills	0	0	4	2
2	ECE528	DSP for VLSI	3	0	0	3
3	ECE546	DSP for VLSI Lab	0	0	2	1
4	ECE529	ASIC Design and FPGAs	3	0	0	3
5	ECE529	ASIC Design and FPGAs PBL	0	0	2	0
6	ECE547	ASIC Design Lab	0	0	2	1
7	ECE525	Low power VLSI Design (PE-2)	3	0	0	3
8	ECE527	Advanced Digital VLSI Design (PE-3)	3	0	0	3
	ECE526	VLSI Testing and fault Tolerance	3	0	0	3
<b>SEMESTER III</b>						
<b>Sl. No.</b>	<b>Course Code</b>	<b>Course Title</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
1	ECE614	Embedded System Design	3	0	0	3
2	ECE615	Physical Design Automation (PE-4)	3	0	0	3
3	ECE617	System on Chip Design (PE-5)	3	0	0	3
4	ECE654	Embedded System Lab	0	0	2	1
5	ECE619	Project-1	0	0	10	5
<b>SEMESTER IV</b>						
<b>Sl. No.</b>	<b>Course Code</b>	<b>Course Title</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
1	ECE629	Project-2 (PC)	0	0	30	15
<b>Total Credits</b>						<b>15</b>